New Application: Yoshinori SHIZUNO Filed: November 7, 2003 4 sheets of drawings FIGS.1A-4 RABIN & BERDO, P.C. Atty Docket: OKI 380

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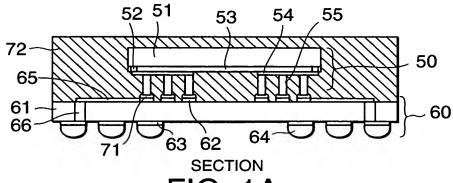
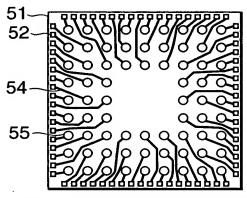
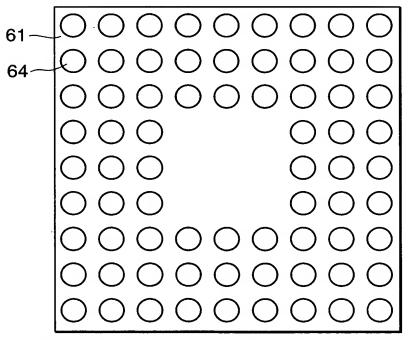


FIG. 1A



PLAN OF SEMICONDUCTOR CHIP

FIG. 1B



SEMICONDUCTOR DEVICE OF FIRST EMBODIMENT OF THE PRESENT INVENTION

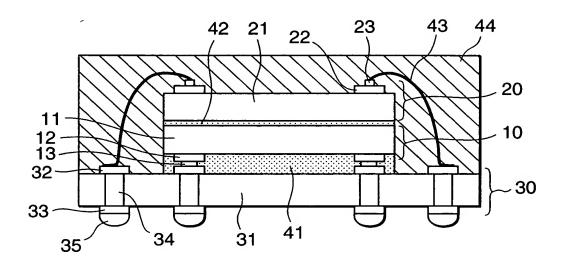
FIG. 1C

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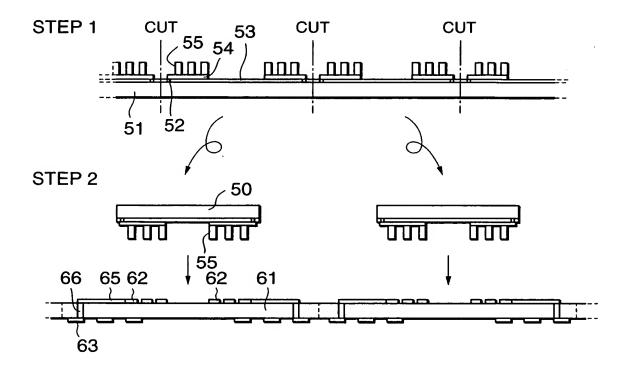
PRIOR-ART SEMICONDUCTOR DEVICE

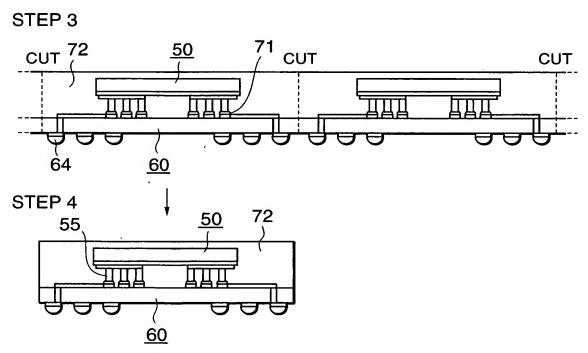
FIG. 2

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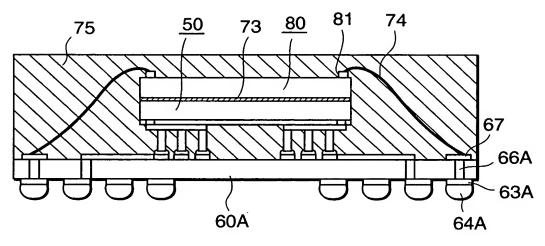
MANUFACTURING METHOD OF FIG. 1

FIG. 3

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SEMICONDUCTOR DEVICE OF SECOND EMBODIMENT OF THE PRESENT INVENTION

FIG. 4